



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

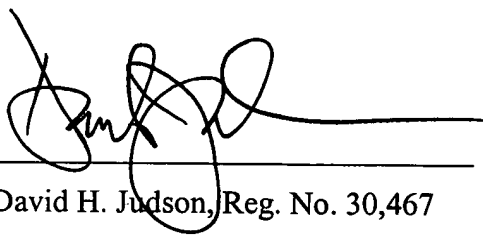
Applicant: Cutts et al.
Serial Number: 10/806,488
Filing Date: March 23, 2004
Art Unit: 2183
For: **FIXED CONTENT
DISTRIBUTED DATA
STORAGE USING
PERMUTATION RING
ENCODING**

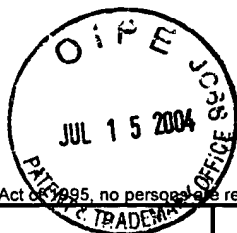
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Pursuant to the duty of disclosure under Rule 56, Applicants desire to bring to the attention of the Office references that are believed to be material to the patentability of the above-identified application. This submittal is made in accordance with Rules 97 and 98 and Section 609 of the Manual of Patent Examining Procedure. A copy of each reference is included.

Respectfully submitted,

By: 
David H. Judson, Reg. No. 30,467
ATTORNEY FOR APPLICANTS



PTO/SB/08B (08-03)

Approved for use through 07/31/2006. OMB 0651-0031

U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

Under the Paperwork Reduction Act of 1995, no person is required to respond to a collection of information unless it contains a valid OMB control number.

Substitute for form 1449/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Complete if Known

Application Number 10/806,488

Filing Date March 23, 2004

First Named Inventor Cutts

Art Unit 2183

Examiner Name

Attorney Docket Number Archivas - 0001

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		GOOD et al., "A Robust Storage System Architecture," Technical Report: CS-95-10, Dept. of Computer Science, University of Waterloo, March 9, 1995	
		BESTAVROS, "A VLSI Chip for the Real-time Information Dispersal and Retrieval for Security and Fault-Tolerance," Harvard University, September, 1989	
		PLANK, "A Tutorial on Reed-Solomon Coding for Fault-Tolerance in RAID-like Systems," Technical Report UT-CS-96-332, Dept. of Comp. Science, Univ. of Tennessee, July 19, 1996	

Examiner Signature		Date Considered	
-----------------------	--	--------------------	--

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached. This collection of information is required by 37 CFR 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 (1-800-786-9199) and select option 2.